

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re application of:

Catalasan et al.

Appl. No. To be assigned

Filed: Herewith on October 31, 2003

For: **Method for Manufacturing a
Memory Cell for Modification of
Revision Identifier in an
Integrated Circuit Chip**

Confirmation No. To be assigned

Art Unit: To be assigned

Examiner: To be assigned

Atty. Docket: 1875.4360003

**Authorization to Treat a Reply as Incorporating an Extension of Time
under 37 C.F.R. § 1.136(a)(3)**

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

The U.S. Patent and Trademark Office is hereby authorized to treat any concurrent or future reply that requires a petition for an extension of time under this paragraph for its timely submission, as incorporating a petition for extension of time for the appropriate length of time. The U.S. Patent and Trademark Office is hereby authorized to charge all required extension of time fees to our Deposit Account No. 19-0036, if such fees are not otherwise provided for in such reply.

Respectfully submitted,

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Date: 10/31/03

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